



FACULTY PROFILE



Name : **Dr. Sirisha Bogimi**

Qualification : M.Tech., Ph.D.

Designation : Assistant Professor

Department : Electrical Engineering,
University College of Engineering,
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Areas of Interest : Power Electronics and Electric Drives.

Sl.No	Course	Specialization	Univ. / College
1	B.E	Electrical Engineering	University College of Engineering, Osmania University Hyderabad.
2	M.Tech.	Power Electronics	JNTU College of Engineering, Hyderabad.
3	Ph.D	Electrical Engineering	Osmania University Hyderabad.

Experience : 15 years

Sl.No.	Company / Institute	Designation	Years
1	APGENCO	Assistant Engineer	2005-2007
2	University College of Engineering(A), Osmania University, Hyderabad	Assistant Professor	2007-Till date

Sl.No	Position held	Department
1.	I/c. Seminar Library	Dept. of EEE, UCE, O.U.
2.	Faculty Adviser, M.E.(P.E.S)	Dept. of EEE, UCE, O.U.
3.	Faculty Adviser, Ph.D	Dept. of EEE, UCE, O.U.
4.	I/c Examination cell	UCE, Osmania University

Research Publications:

International Journals : 07
Conferences : 06



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LIST OF PUBLICATIONS

INTERNATIONAL JOURNALS:

- [1]. B.Sirisha, Nazeemuddin “A Novel Five-Level Voltage Source Inverter interconnected to Grid with SRF controller for voltage synchronization” *Bulletin of Electrical Engineering and Informatics*(under Review).
- [2]. B.Sirisha, Akshay Kapil Gajoju “A Grid interconnected five-level inverter for voltage synchronization using SRF controller and SVPWM with photo voltaic application” *International Journal of Research and Analytical Reviews*, vol 8,Issue 3,July 2021pp919-924.
- [3]. B.Sirisha, P.Satishkumar “Simplified Space Vector Pulse Width Modulation Based on Switching Schemes with Reduced Switching Frequency and Harmonics for Five Level Cascaded H-Bridge Inverter”, *International Journal of Electrical and Computer Engineering*, Vol. 8 No5.2017(ISSN: 2088-8708)(SJR:0.28).
- [4]. B.Sirisha, P. Satish Kumar “A Simplified and Generalized Space Vector Pulse Width Modulation Method Including Over Modulation Zone for Seven Diode Clamped Inverter - FPGA Implementation”, *International Journal of Power Electronics* Vol.10 No.4 (ISSN: 1756-6398,Inderscience Publishers).
- [5]. B.Sirisha, P.Satish kumar “A Space Vector Pulse Width Modulation Technique for Five Level Cascaded H-Bridge Inverter Including Over Modulation Region with FPGA Implementation”, *International Journal of power Electronics and Drives*, Vol. 8, No.3,September2017(ISSN : 2088-8694) (SJR:0.25)
- [6]. B.Sirisha, P.Satishkumar“A Space Vector Pulse Width Modulation Technique for Five Level Cascaded H-Bridge Inverter Including Over Modulation Region”, *International Journal of Inventions In Electronics & Electrical Engineering* Vol 2, December 2017.(ISSN NO:2347-6982),(Impact factor:2.51).
- [7]. B.Sirisha,P.Satishkumar“A Simplified Space Vector Pulse Width Modulation Method for Cascaded H-bridge Multilevel Inverters”.*International Journal of Advanced Research in Electrical Electronics and Instrumentation Engineering*, Vol 3,Issue 12,December 2014.(ISSN NO: 2278-8875)(Impact factor: 5.621).
- [8]. B.Sirisha, P.Satishkumar. “Three Phase Two Leg Neutral Point Clamped Converter with output DC Voltage Regulation and Input Power Factor Correction”. *International Journal of Power Electronics and Drive System*, Vol .2, No. 2 , June 2012.(ISSN: 2088-8694) (SJR:0.28).
- [9]. N.Susheela, B.Sirisha, P.Satish kumar “Hybrid Topologies of Multilevel Converter for Current Waveform Improvement”, *International Journal of Inventive Engineering and Sciences*, Vol.1, Issue 4, March 2013, (ISSN:2319-9598), (Impact Factor : 3.47).

INTERNATIONAL CONFERENCES:

- [10] B.Sirisha, P.Satish kumar “SVPWM Based Generalized Switching Schemes for Sevel level DCMLI Including Over Modulation Operation – FPGA Implementation”, TENCON-2019, IEEE Region 10 Conference pp 2135-2142.
- [11]. B.Sirisha, P.Satishkumar“A Simplified Space Vector PWM for Cascaded H- Bridge Inverter including Over Modulation Operation”,*13th International IEEE India Conference INDICON 2016*, at IISC, Bengaluru,India. December 16-18, 2016.
- [12]. B.Sirisha, P.Satish kumar“Implementation of FPGA based Space Vector PWM Methodfor Five Level Cascaded Inverter”,*IEEE 7th Power India International Conference (PIICON 2016)* at Bikaner Rajasthan ,November 25-27 2016.



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- [13]. N.Susheela, B.Sirisha, P.Satish Kumar “Hybrid Topologies of Multilevel Converter for Current Waveform Improvement”, *International Journal of Inventive Engineering and Sciences, Vol.1, Issue 4, March 2013, (ISSN: 2319-9598), (Impact Factor : 3.47).*
- [14]. B.Sirisha, P.Satish Kumar “DTC control schemes with space vector modulation control strategy for three level NPC-VSI fed induction motor drive”, *International conference on Advances in Engineering and Technology (ICAET-2013) at DR M.G.R Educational and Research Institute University .April 5th and 6th 2013.*
- [15]. B.Sirisha, N.Susheela, P.Satish Kumar “Implementation of High Step up DC-DC converter using Cascade Technique from Fuel Cell Electric Conversion System” *Advancements in Electrical & Electronics Engineering, (NCAEEE-2012) Department of Electrical and Electronics Engineering, Sri Venkateswara College of Engineering, Sriperumbudur, Tamilnadu on 17-18 Feb, 2012.*

Refresher Courses/ Workshops/ Conferences Attended:

1. “Orientation Programme”, conducted by UGC-Academic Staff College, Osmania University, Hyderabad, Osmania University, Hyderabad, 01 – 28 June, 2010.
2. Three Week Refresher course on “Advances in Power Electronics and their Applications to Drives and Power Systems” conducted by UGC-Academic Staff College, JNTU, Hyderabad, 10 - 29 Nov, 2008.
3. Two Week Staff Development Programme on “High Voltage Testing Measurements & Gas Insulated Substations” AICTE-Department of Electrical and Electronics Engineering, JNTU, Hyderabad, 12 - 24 Dec 2011.
4. A workshop on “Power Quality”, conducted by *University College of Engineering, Osmania University, Hyderabad, 28-29 Sep, 2007.*
5. A workshop on “EDSA Software Applications to Power Systems Power Quality”, conducted by *University College of Engineering, Osmania University, Hyderabad, 05-06 Dec, 2008.*
6. The UGC sponsored Three Week Refresher course on “Advances in Power Electronics and Electric Drives”, conducted by *UGC-Academic Staff College, J.N.T.University, Hyderabad, 09-28 August, 2010.*
7. Two week “Faculty Development Programme in Entrepreneurship” (sponsored by NSTEDB, DST, New Delhi) held during 01-06-2011 to 15-06-2011.

Workshops Organized

1. A five day short term course on “Engineering Research Methodology” at University College of Engineering, Osmania University, Hyderabad from 13th to 17th may 2019.
2. A one-day workshop on “Recent Trends in Renewable Energy Systems and Hybrid Electric Vehicles” at University College of Engineering, Osmania University, Hyderabad on 16th November 2019.
3. A one-day workshop on “Induction Program for Research Scholars” at University College of Engineering, Osmania University, Hyderabad on 20th April 2019.
